

S/N 09/135,413PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al.

Examiner: Viet Q. Nguyen

Serial No.: 09/135,413

Group Art Unit: 2818

Filed: August 14, 1998

Docket: 303.354US2

Title: METHOD FOR OPERATING A MEMORY DEVICE HAVING AN
AMORPHOUS SILICON CARBIDE GATE INSULATORCOMMUNICATION CONCERNING RELATED APPLICATION(S)Mail Stop 313(c)
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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OFFICE OF PETITIONS

Applicants would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date/Issue Date</u>	<u>Attorney Docket</u>	<u>Title</u>
10/789203	February 27, 2004	303.356US4	OPERATING A MEMORY DEVICE
11/073773	March 7, 2005	303.324US5	TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND METHODS OF FABRICATION AND USE


Continuations and divisionals may be later filed on the cases listed above, or cited to the Examiner in any previous Communication Concerning Related Applications. Applicants request that the Examiner review all continuations and divisionals of the above-listed or previously-cited patent applications before allowing the claims of the present patent application.

Respectfully submitted,
LEONARD FORBES ET AL.

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I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date shown below.

Kacia Lee
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Sept. 20, 2005
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